## **EAST Search History**

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L11	77	10 and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble \$1shoot trouble \$1shooted trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3 (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	N	2008/05/07
L10	77	8 not 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 15:43
L9	26	(US-20060236274-\$ or US-20040133833-\$ or US-20020173942- \$).did. or (US- 7051303-\$ or US- 6785873-\$ or US- 6591403-\$ or US- 6539523-\$ or US- 6493852-\$ or US- 6389379-\$ or US- 6026230-\$ or US- 6009256-\$ or US- 6510541-\$ or US- 6510541-\$ or US- 6212489-\$ or US- 6496972-\$ or US- 6574778-\$ or US- 6374205-\$ or US-	US-PGPUB; USPAT	OR	Z O	2008/05/07

 L8	78	7103863-\$ or US- 6421808-\$ or US- 5748486-\$ or US- 5515526-\$ or US- 7206731-\$).did.	US-PGPUB;	OR	ON	2008/05/07
			USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB			15:39
L7		((simulat\$3 emulat\$3) WITH (verifi\$6 verify \$3 validat\$3 test\$3 "logic checking" logic \$1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimised optimisation optimising)) same ((reconstruct reconstructor reconstructor reconstruction respective stocking reconstruction ressemble reassembled reassembled reassembled reassembled reassembled reassembled ressembled ressembled restored re \$1assembled re \$1assembled restored re \$1assembled restored re \$1assembled restored re \$1assembled restored re \$1assembled restoration re\$1store re\$1stored re \$1storing re \$1storing re \$1storation) SAME (list listed listing enumerate enumerated enumerated enumeration e	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB		ON	2008/05/07

		component sub \$1 component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble \$1 shoot trouble \$1 shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3 (co adj verifi\$6) co\$1 verifi\$6 (co adj verify\$3) co \$1 verify\$3 (co adj validat\$3) co\$1 validat\$3))				
L6	O	WITH (verifi\$6 verify	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	Z	2008/05/07

		RTL REGISTER \$1TRANSFER \$1LEVEL CIRCUIT ckt MICRO \$1CIRCUIT ASIC MODEL logic)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooted troubleshooted trouble \$1shoot trouble \$1shooted trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3) co \$1simulat\$3 (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat \$3))				
L5	·O	((simulat\$3 emulat\$3) WITH (verifi\$6 verify \$3 validat\$3 test\$3 "logic checking" logic \$1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimising)) same ((reconstruct reconstructd reconstructor reconstructing reconstructing reconstructing reconstructing resonstruction re \$1constructed re \$1constructing re \$1constructing re \$1constructing re \$1construction reassemble reassembled reassembled reassembled re \$1assembled re	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	NOTE TO THE TOTAL THE TOTA	S	2008/05/07 15:24

	\$1storing re \$1storation) WITH (list		
	listed listing		
	enumerate		
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anna.	data\$1base databank		
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	repository) ((sav\$3 stor		
	\$3) NEAR data))		
	WITH (node net wire		
	gate logic cell block		
	subblock sub\$1block		
	circuit sub\$1circuit		
	subcircuit object module submodule		
anna.	sub\$1module		
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anna a	subcomponent sub		
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	and ((bug failure error		
mm	mistake debug		
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· · · · · ·	\$1shooting) WITH		
mm	(simulat\$3 emulat\$3		
	(co adj simulat\$3) co		
anna a	\$1simulat\$3 (co adj		
	verifi\$6) co\$1verifi\$6		
	(co adj verify\$3) co		
	\$1verify\$3 (co adj		
mann	validat\$3) co\$1validat	***************************************	
: §	\$3))		

L4	68	((simulat\$3 emulat\$3)	US-PGPUB;	OR	ON	2008/05/07
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		\$3 validat\$3 test\$3	FPRS; EPO;			10.10
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	,,,,,	"logic checking" logic	JPO; DERWENT;			
		\$1checking) WITH	IBM_TDB			
		(optimize optimized				
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		listing enumerate				
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		data\$1base databank				
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	mm.	repository) ((sav\$3 stor				
		\$3) NEAR data))				
		SAME (node net wire				
		gate logic cell block				
		subblock sub\$1block				
		circuit sub\$1circuit				
		subcircuit object				
		module submodule				
		sub\$1module			-	
		component				
		subcomponent sub				
		\$1component model))				
		and ((bug failure error				
	umm	mistake debug				
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		troubleshoot				***************************************
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·		troubleshooting trouble \$1shoot trouble \$1shooted trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3) co \$1simulat\$3 (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat \$3))			
	37	((simulat\$3 emulat\$3) WITH (verifi\$6 verify \$3 validat\$3 test\$3 "logic checking" logic \$1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimised optimized optimizing) same ((optimize optimized optimizing optimization minimize minimized minimizing minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplified) WITH (list listed listing enumerate enumerated enumeration enumerating database data\$1base databank data\$1bank (data adj repository) ((sav\$3 stor \$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub\$1block circuit sub\$1circuit subcircuit object module submodule sub\$1module component	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	O	15:11

аналинаннаннаннаннаннаннаннаннаннаннаннаннан	subcomponent sub \$1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble \$1shoot trouble \$1shooted trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3) co \$1simulat\$3 (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat \$3))				
 4 4	((simulat\$3 emulat\$3) WITH (verifi\$6 verify \$3 validat\$3 test\$3 "logic checking" logic \$1 checking) WITH (optimize optimized optimization optimizing optimal optimised optimisation optimised optimised optimized optimized optimized optimized optimizing optimal optimizing optimal optimizing optimal optimizing optimal optimizing optimal optimized optimised optimized o	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	N	2008/05/07 15:03

		sub\$1module component subcomponent sub \$1component model)) and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosing diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble \$1shoot trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3) co \$1simulat\$3 (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat \$3))				
L1	66	((simulat\$3 emulat\$3) WITH (verifi\$6 verify \$3 validat\$3 test\$3 "logic checking" logic \$1checking) WITH (optimize optimized optimization optimizing optimal optimum optimise optimised optimisation optimised optimisation optimising)) same ((list listed listing enumerate enumerated enumeration enumerating database data\$1base databank data\$1bank (data adj repository) ((sav\$3 stor \$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub\$1block circuit sub\$1circuit subcircuit object module submodule sub\$1module component subcomponent sub \$1component model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON.	2008/05/07

		and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble \$1shoot trouble \$1shooted trouble \$1shooted trouble \$1shooted trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3 (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat\$3))				
S63	26	S61 and (bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosis diagnostics troubleshoot troubleshooting trouble \$1shoot trouble \$1shooted trouble \$1shooted trouble \$1shooting)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/01
S62	21	S61 and ((bug failure error mistake debug debugged debugging test tested testing check checked checking diagnose diagnosed diagnosis diagnostics troubleshoot troubleshooted troubleshooting trouble \$1shoot trouble \$1shooted trouble \$1shooting) WITH (simulat\$3 emulat\$3 (co adj simulat\$3) co \$1simulat\$3 (co adj verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	<b>N</b>	2008/05/01 15:13

		validat\$3) co\$1validat \$3))				
S61	6 21	(US-20060236274-\$ or US-20040133833-\$ or US-20020173942- \$).did. or (US- 7051303-\$ or US- 6785873-\$ or US- 6591403-\$ or US- 6539523-\$ or US- 6493852-\$ or US- 6389379-\$ or US- 6026230-\$ or US- 6009256-\$ or US- 6009256-\$ or US- 6510541-\$ or US- 6088821-\$ or US- 6212489-\$ or US- 6496972-\$ or US- 6496972-\$ or US- 6374205-\$ or US- 6374205-\$ or US- 6345378-\$ or US- 5748486-\$ or US- 5748486-\$ or US- 5748486-\$ or US- 57206731-\$).did.	US-PGPUB; USP <b>A</b> T	OR	NON NON THE RESERVENCE OF THE PROPERTY OF THE	2008/05/01
S60	26	\$49 and (verifi\$6 verify \$3 validat\$3 test\$3 "logic checking" logic \$1checking (co adj verifi\$6) co\$1verifi\$6 (co adj verify\$3) co \$1verify\$3 (co adj validat\$3) co\$1validat \$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	O N	2008/04/30
S59	25	S49 and simulat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30 21:45

S56	1	\$49 and ((reconstruct	US-PGPUB;	OR	ON	2008/04/30
6 5 6	1	S49 and ((reconstruct reconstructed reconstruction re \$1 construct re \$1 construct re \$1 construction re \$1 construction re \$1 construction reassemble reassembled reassembled reassembled reassembled resored restored restored restored restored restored restored restored restored re \$1 stored re \$1 storing re \$	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB		<b>Z</b>	2008/04/30 18:00
555	6	S49 and ((reconstruct reconstructed reconstructing reconstruction re \$1 construct re \$1 constructed re \$1 construction reassemble reassembled reassembled reassembled ressembled re \$1 assembled re \$1 assembl	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	NON NO	2008/04/30 18:00

		\$1storation) WITH (NETLIST NET\$1LIST RTL REGISTER \$1TRANSFER \$1LEVEL CIRCUIT ckt MICRO \$1CIRCUIT ASIC MODEL logic))				
$\sim$	0	S49 and ((reconstruct reconstructed reconstructing reconstruction re \$1 constructed re \$1 construction re \$1 construction re \$1 construction reassemble reassembled reassembled reassembled ressembled ressembled restored restoring restored restoring restored restoring restoration re\$1 store re\$1 stored re \$1 storing re \$1 storation) WITH (list listed listing enumerated enumerated enumeration enumeration enumeration enumeration enumeration (sav\$3 stor \$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub\$1 block circuit sub\$1 circuit subcircuit object module submodule subcomponent sub \$1 component model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/30

S53	19	S49 and ((optimize optimized optimized optimizing optimization minimize minimized minimized minimized minimized minimized minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplification simplifying simplified) SAME (list listed listing enumerate enumerated enumeration enumerating database data\$1base databank data\$1bank (data adj repository) ((sav\$3 stor \$3) NEAR data)) SAME (node net wire gate logic cell block subblock sub\$1block circuit sub\$1circuit subcircuit object	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	N	2008/04/30
S52	**************************************	component subcomponent sub \$1component model))  S49 and ((optimize optimized optimizing optimization minimize minimized minimizing minimization reducing reduction reduced reduce resize resized resizing compaction compacting compacted redundancy redundant (timing ADJ shell) abstraction abstracted abstracting simplify simplification simplifying simplified) WITH (list listed listing enumerate enumerated enumeration enumerating database	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	Z	2008/04/30 13:52

		data\$1base databank data\$1bank (data adj repository) ((sav\$3 stor \$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub\$1block circuit sub\$1circuit subcircuit object module submodule sub\$1 module component subcomponent sub\$1component model))				
S51	53 3	S49 and ((optimize optimized optimized optimization optimizing optimal optimise optimised optimi	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	Z 0	2008/04/30
S50	24	S49 and ((list listed listing enumerate enumerated enumeration enumerating database data\$1base databank data\$1bank (data adj repository) ((sav\$3 stor \$3) NEAR data)) WITH (node net wire gate logic cell block subblock sub\$1block circuit sub\$1circuit subcircuit object module submodule sub\$1module component	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OZ	2008/04/30 13:49

		subcomponent sub \$1component model))				
S49	26	(US-20060236274-\$ or US-20040133833-\$ or US-20020173942- \$).did. or (US- 7051303-\$ or US- 6785873-\$ or US- 6591403-\$ or US- 6539523-\$ or US- 6493852-\$ or US- 6389379-\$ or US- 6026230-\$ or US- 6009256-\$ or US- 6009256-\$ or US- 6510541-\$ or US- 6088821-\$ or US- 6212489-\$ or US- 6496972-\$ or US- 6496972-\$ or US- 6374205-\$ or US- 6374205-\$ or US- 5748486-\$ or US- 5748486-\$ or US- 5748486-\$ or US- 57206731-\$).did.	US-PGPUB; USPAT	OR	ON	2008/04/30

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